



孕龍科技股份有限公司
Zeroplus Technology Co., Ltd.

SPECIFICATION

MODEL: B09014-LAP-Compact Flash 4.1-M

PART NO: _____

VERSION: V1.01

Approver		Check	Design
GM	PM		

Customer Confirm

* Please fax the file to
Zeroplus Technology after
signing.

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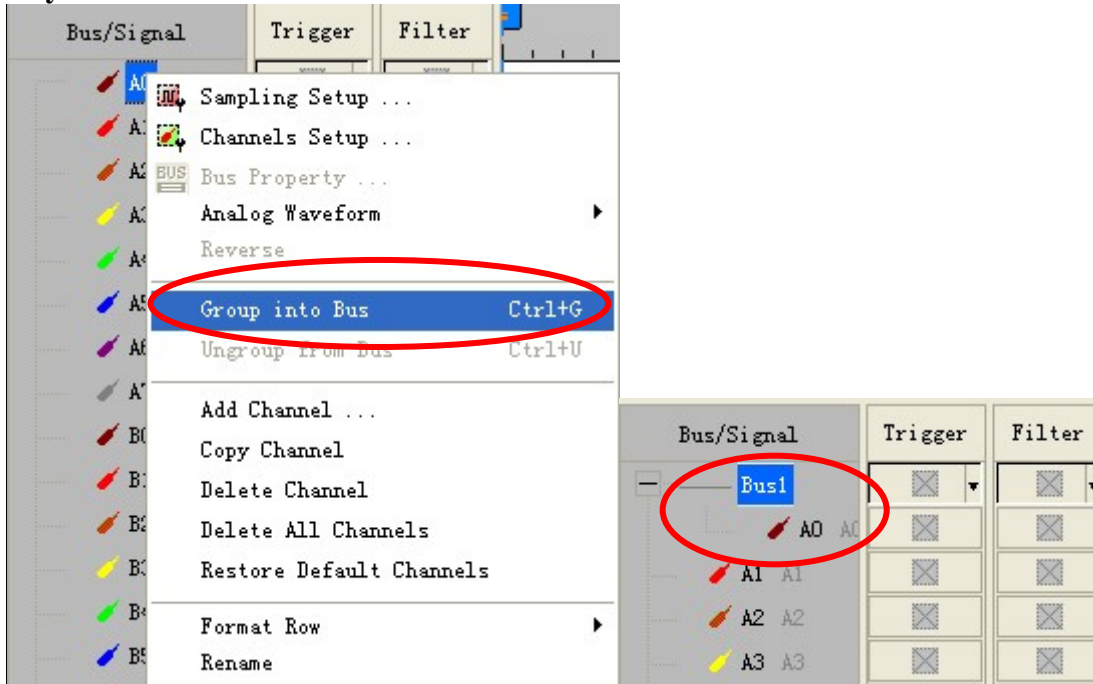
1 Software Register

Please register the software as the following steps:

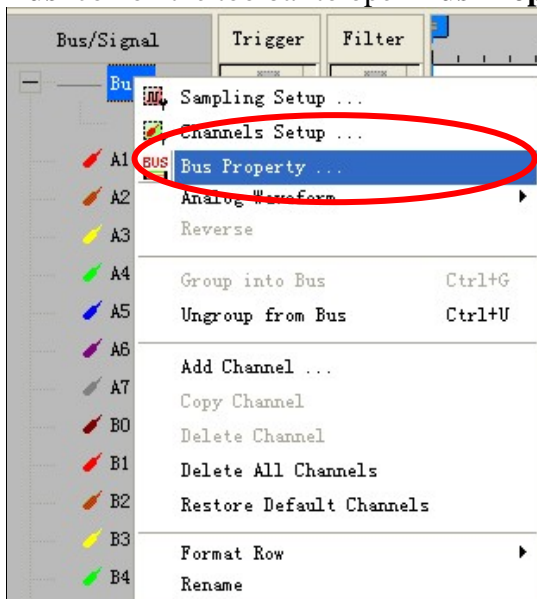
※ Remark1: The registration steps for all protocol analyzers are the same; you can complete the registration by following procedures. Following is an example on how to register the Protocol Analyzer BUS.

※ Remark2: We won't have additional notice for you, when there is any modification of the module specification. If there is some unconformity caused by the module version upgrade, users should take the module software as the standard.

STEP 1. Open the Logic Analyzer and group the unanalyzed channels into **Bus1** by pressing the **Right Key** on the mouse.

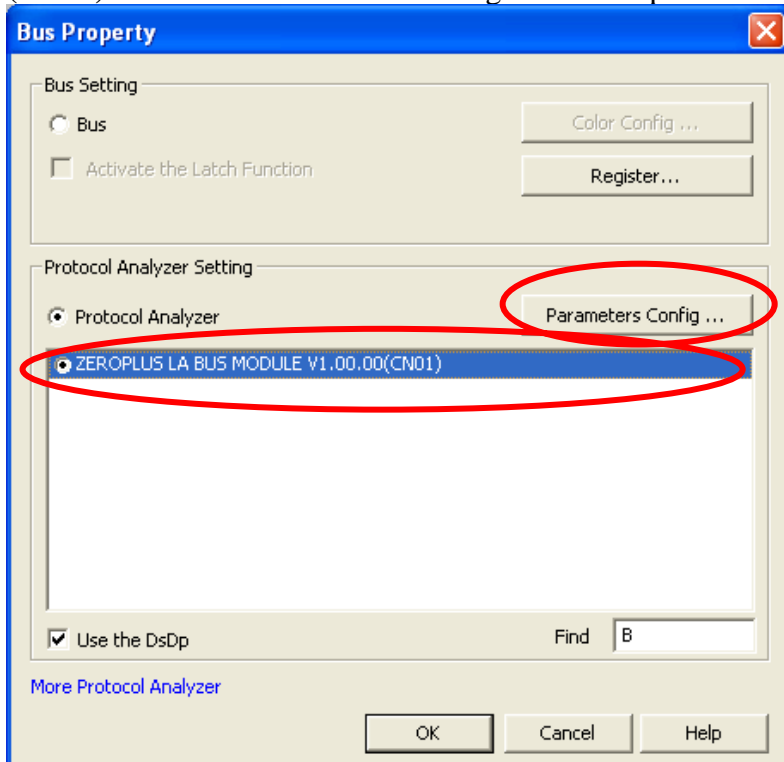


STEP 2. Select **Bus 1**, then press **Right Key** on the mouse to list the menu, then press **Bus Property** or **Bus** icon on the toolbar to open **Bus Property** dialog box.

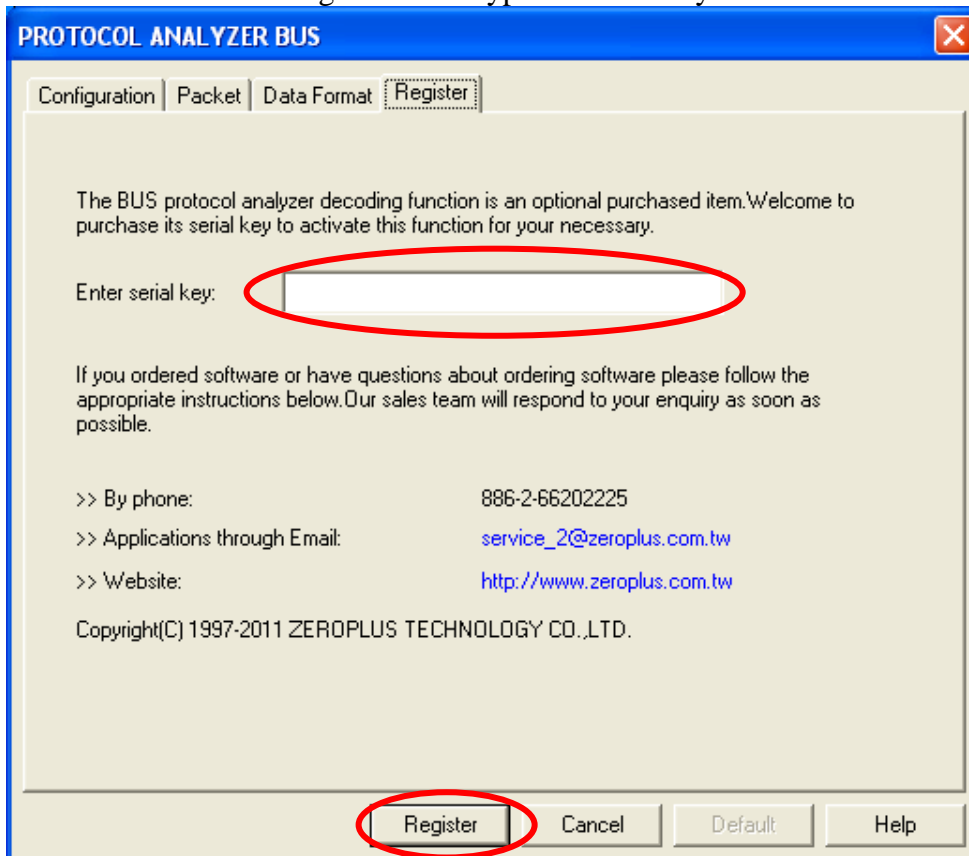




STEP 3. Select the Protocol Analyzer, and then choose **ZEROPLUS LA BUS MODULE V1.00.00 (CN01)**. Next click Parameters Configuration to open Protocol Analyzer Bus dialog box.

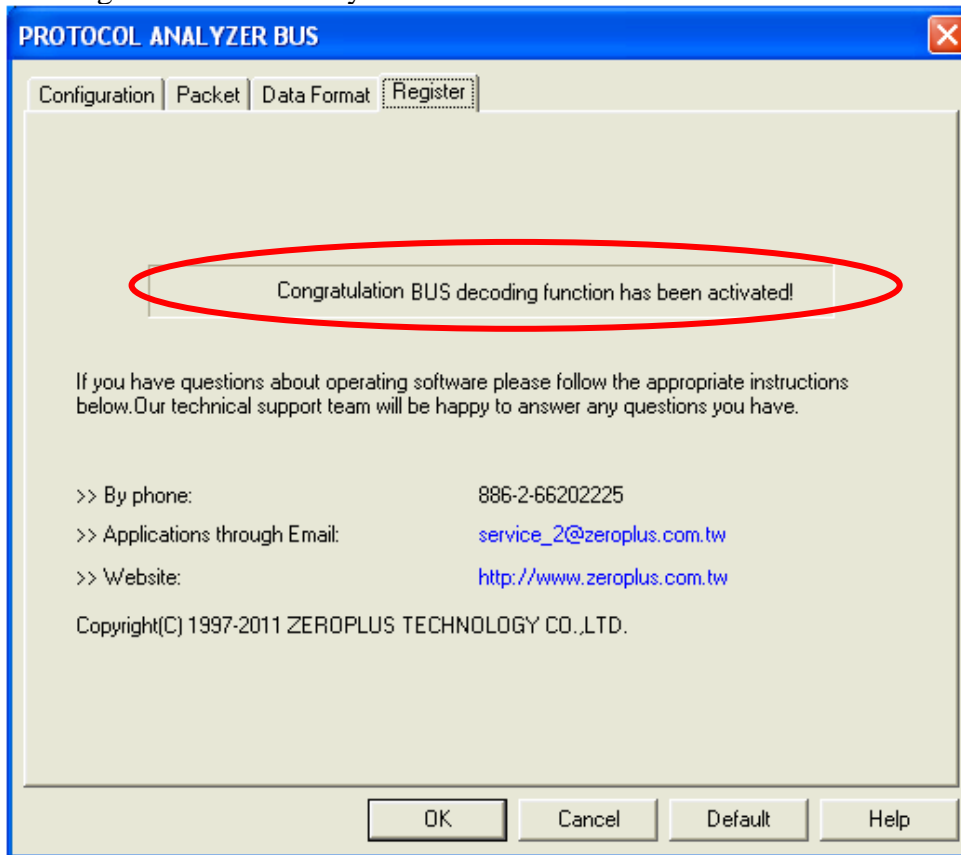


STEP 4. Press Register tab to type the serial key number of BUS. Then press Register.





STEP 5. After pressing the Register button, following dialog box will appear, it denotes that the BUS has been registered successfully.

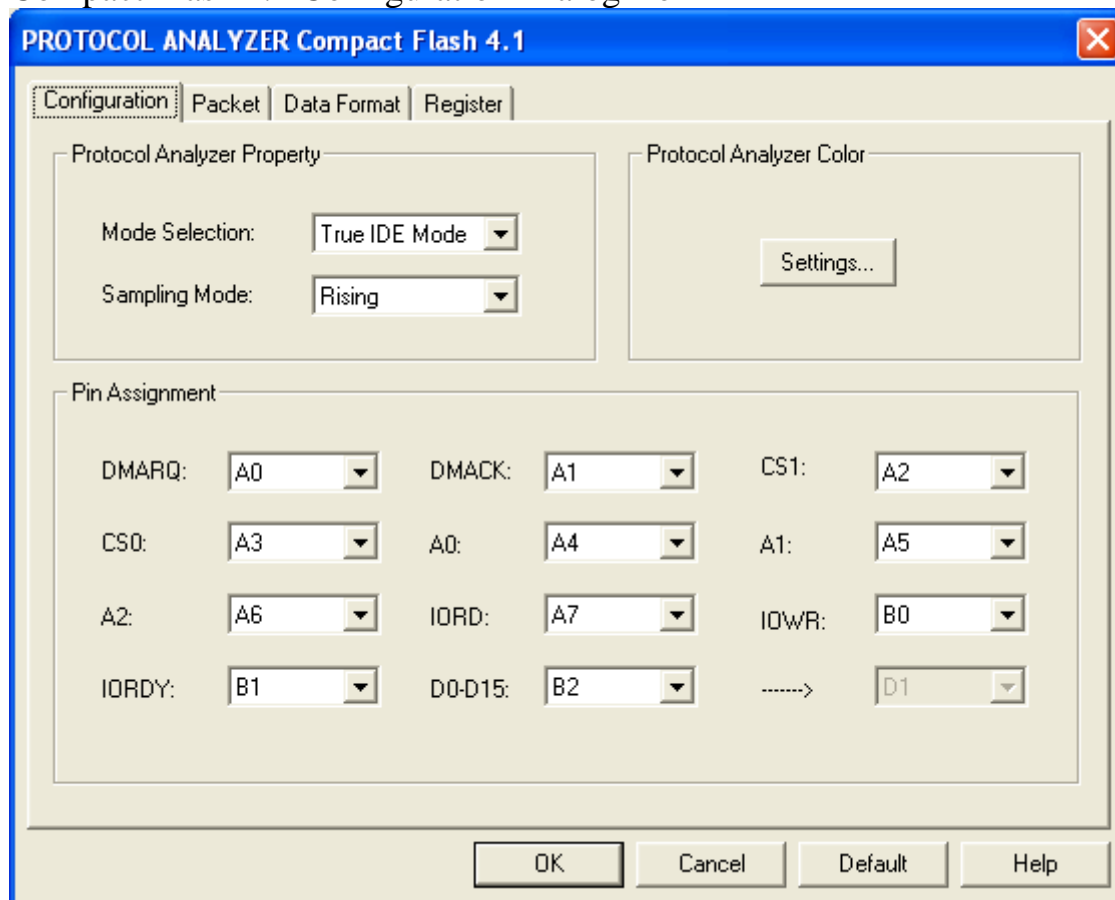




2 User Interface

In the configuration, please refer to the below images to select options of setting Compact Flash 4.1 module.

Compact Flash 4.1 Configuration Dialog Box



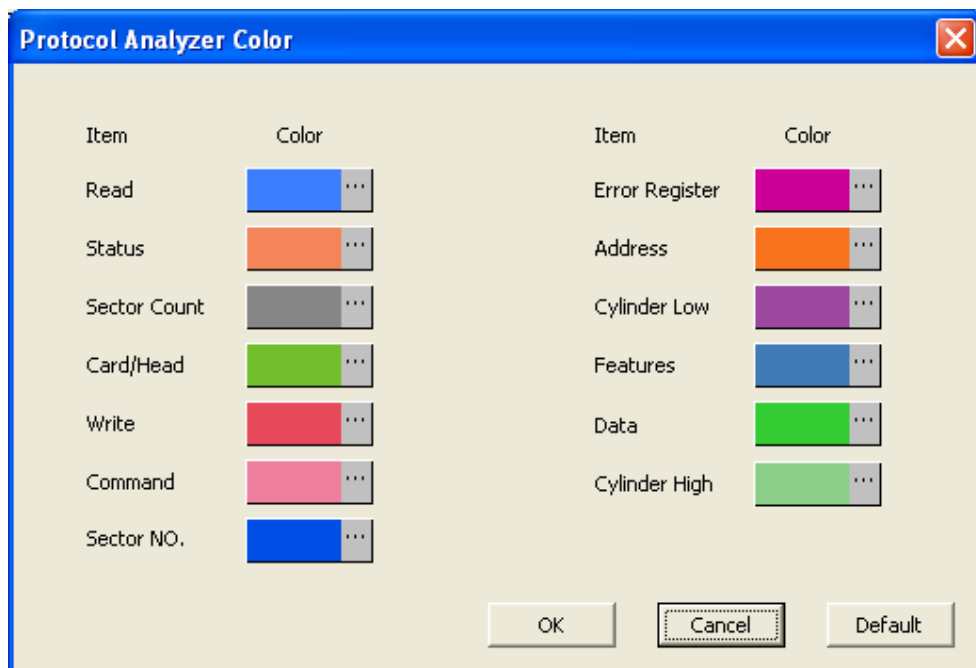
Protocol Analyzer Property:

Mode Selection: Until now only True IDE Mode is available.

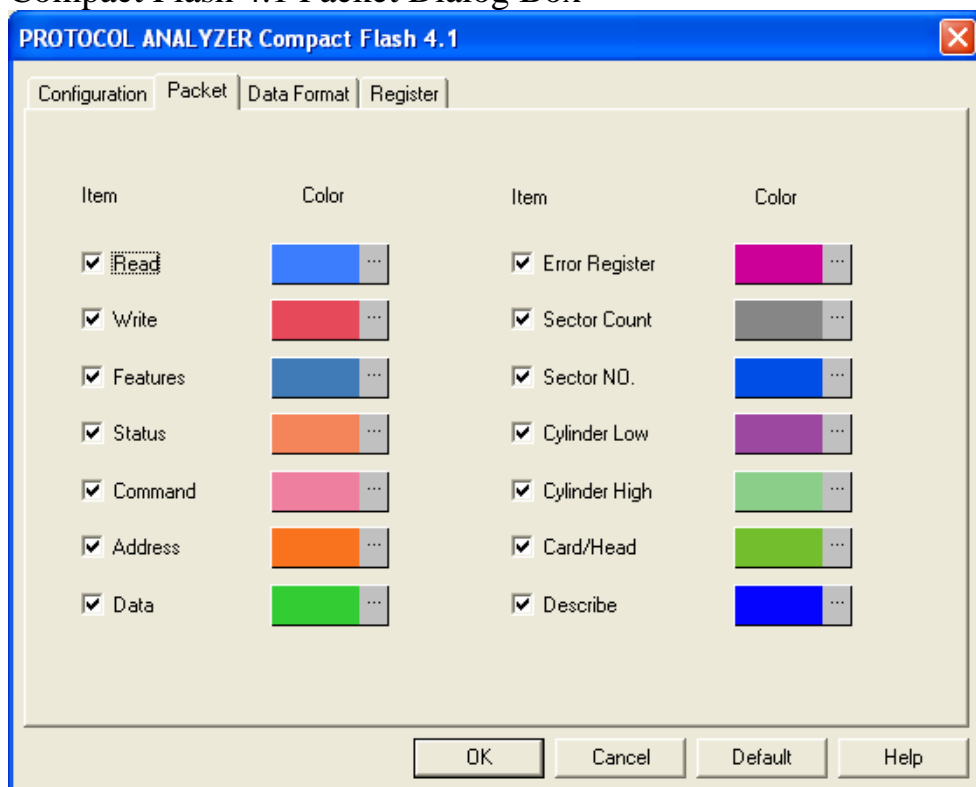
Sampling Mode: The default is Rising for IORD and IOWR; users also can set the **Sampling Mode** to Falling.

Pin Assignment: It needs 26 channels to decode the signal of True IDE Mode, so users must choose the Logic Analyzer with 32 channels or more to capture the signal, the Logic Analyzer with 16 channels can't capture the signal. In the D0-D15 item of the interface, users only can set the Do channel, the following 15 channels increase progressively.

Protocol Analyzer Color: The **Protocol Analyzer Color** can be varied by users.



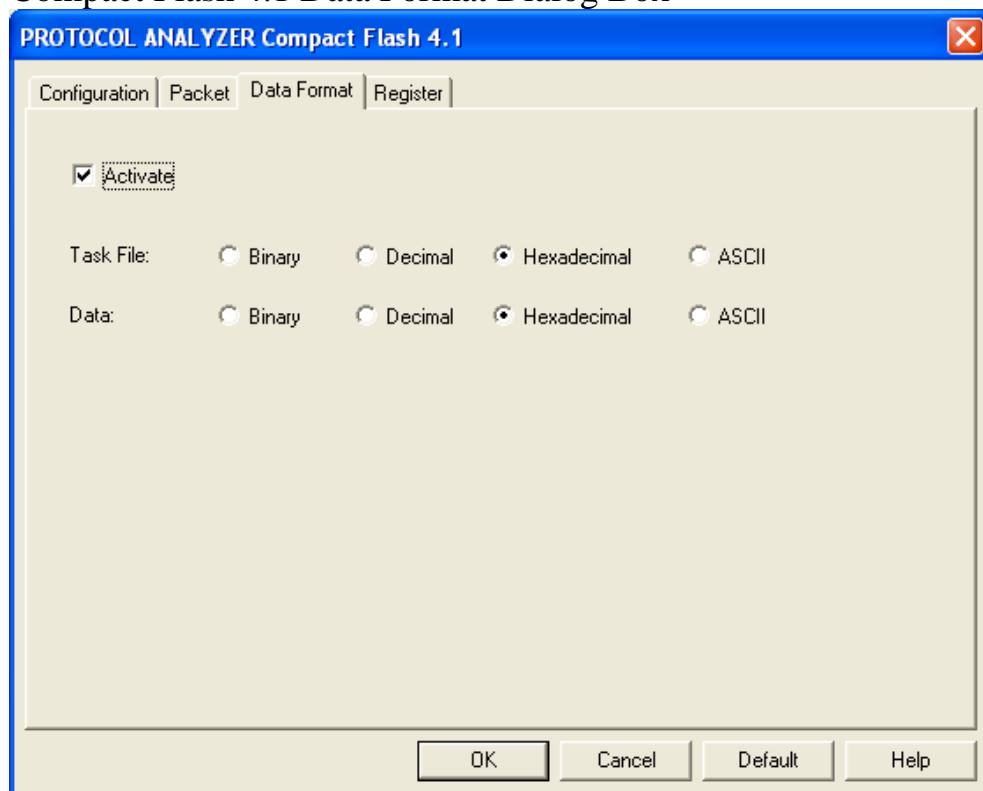
Compact Flash 4.1 Packet Dialog Box



In the Packet part, users can set the items and colors as users' requirements.

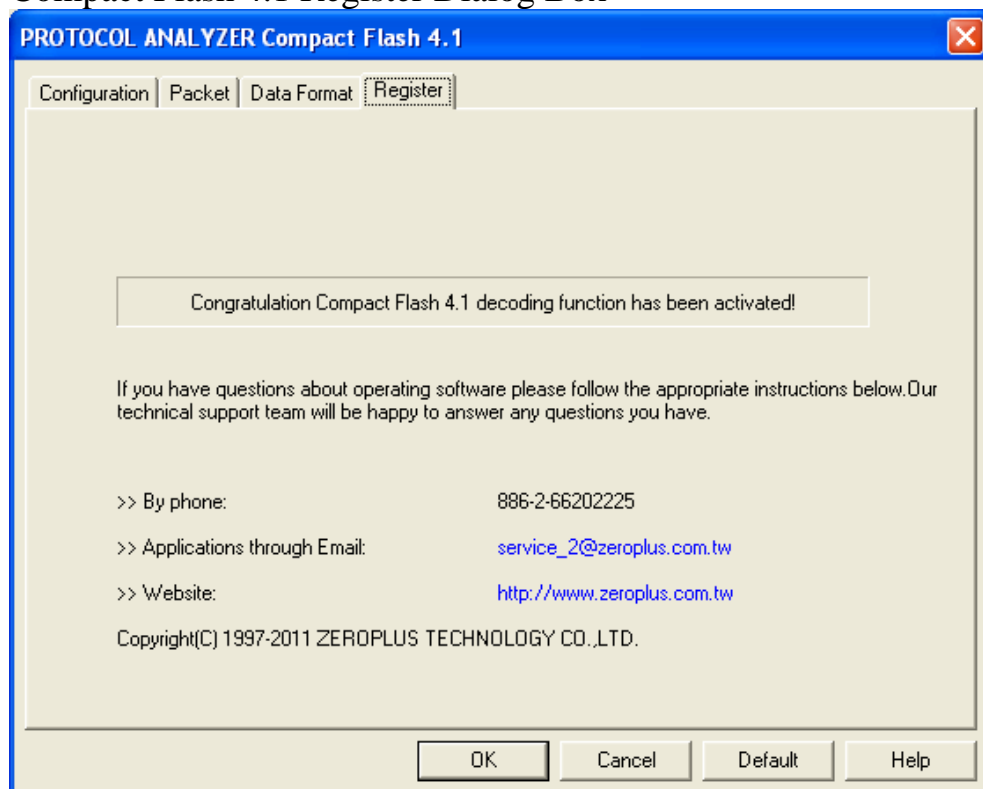


Compact Flash 4.1 Data Format Dialog Box



Users can set the Data Format of Task File and Data as their requirements. When selecting the option, Activate, the data format is decided by the settings in the Protocol Analyzer; when not selecting the option, Activate, the data format is decided by the settings in the main program.

Compact Flash 4.1 Register Dialog Box

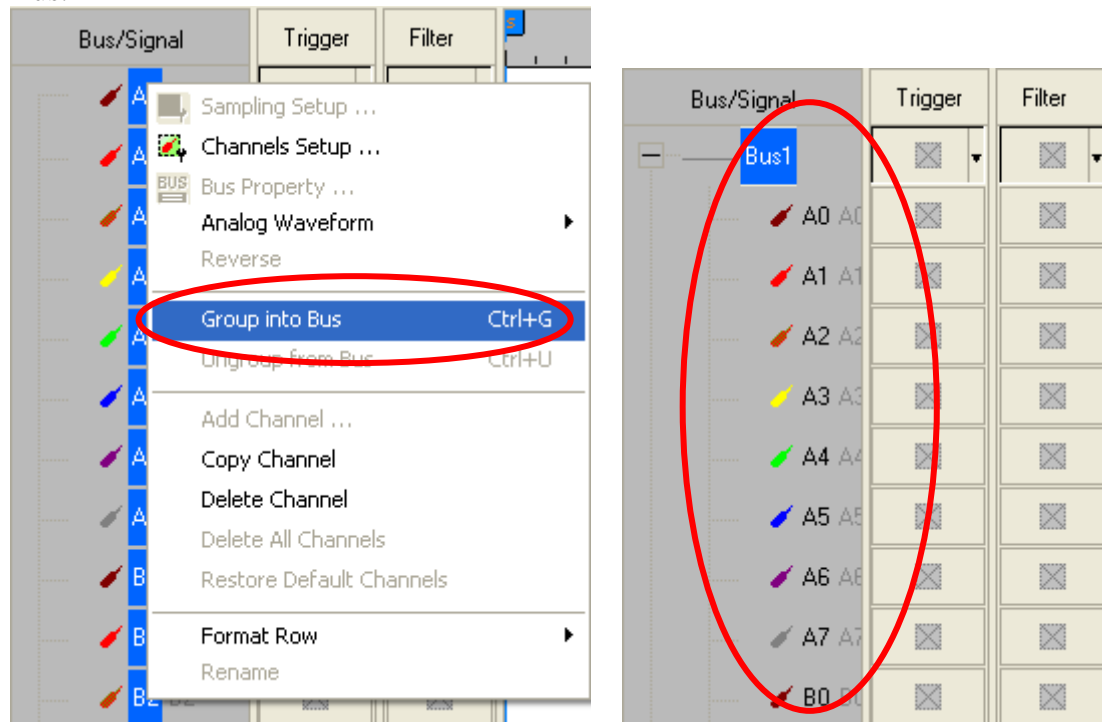


There is written ZeroPlus company information. If you have any questions about software operations, you can contact ZeroPlus by Telephone or Email.

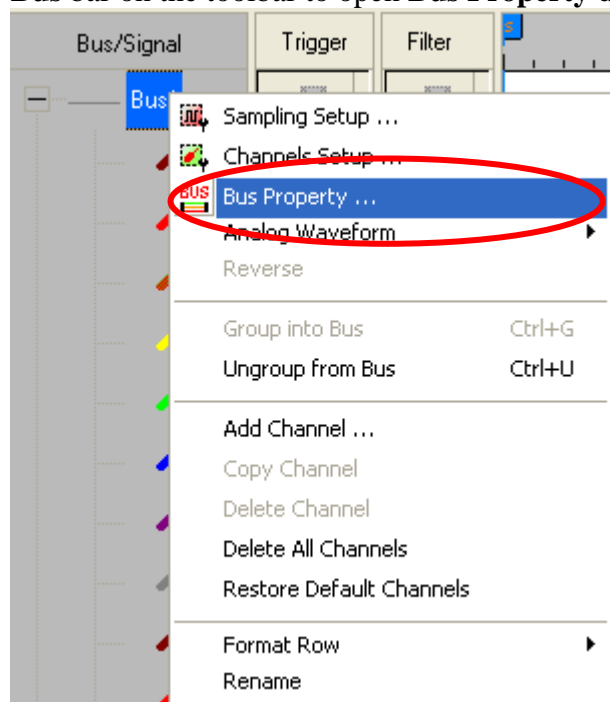


3 Operating Instructions

STEP 1. Group A0-D1 into **Bus1** by pressing the **Right Key** on the mouse. Compact Flash 4.1 needs twenty-six channels to decode signals at least, so it is necessary to group twenty-six or more channels into a Bus.

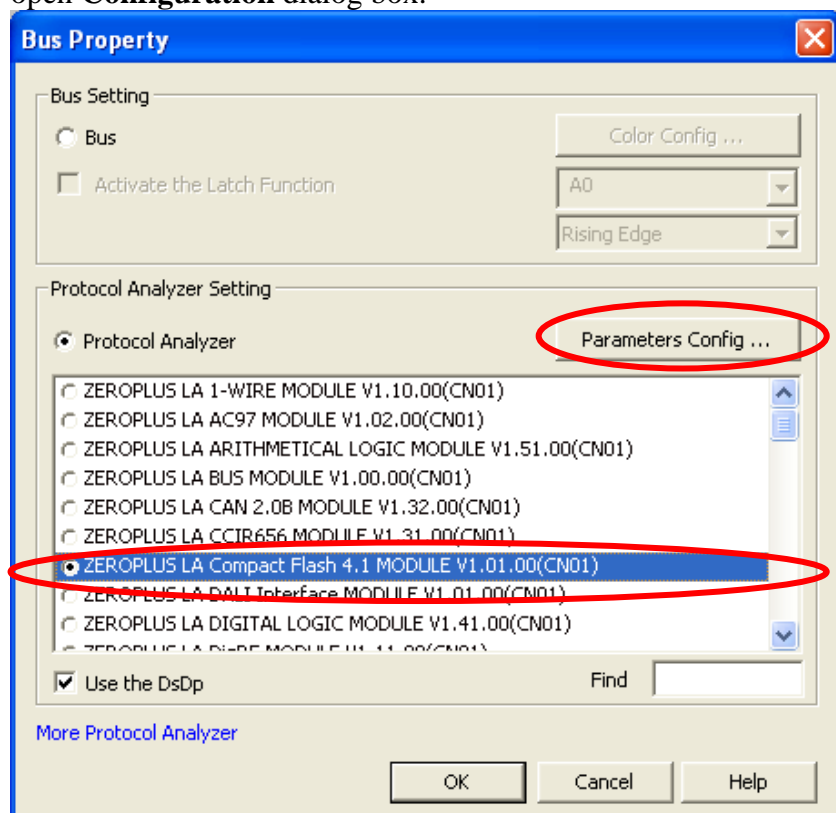


STEP 2. Select **Bus1**, and press **Right Key** on the mouse to list the menu, then press **Bus Property** or **Bus bar** on the toolbar to open **Bus Property** dialog box.

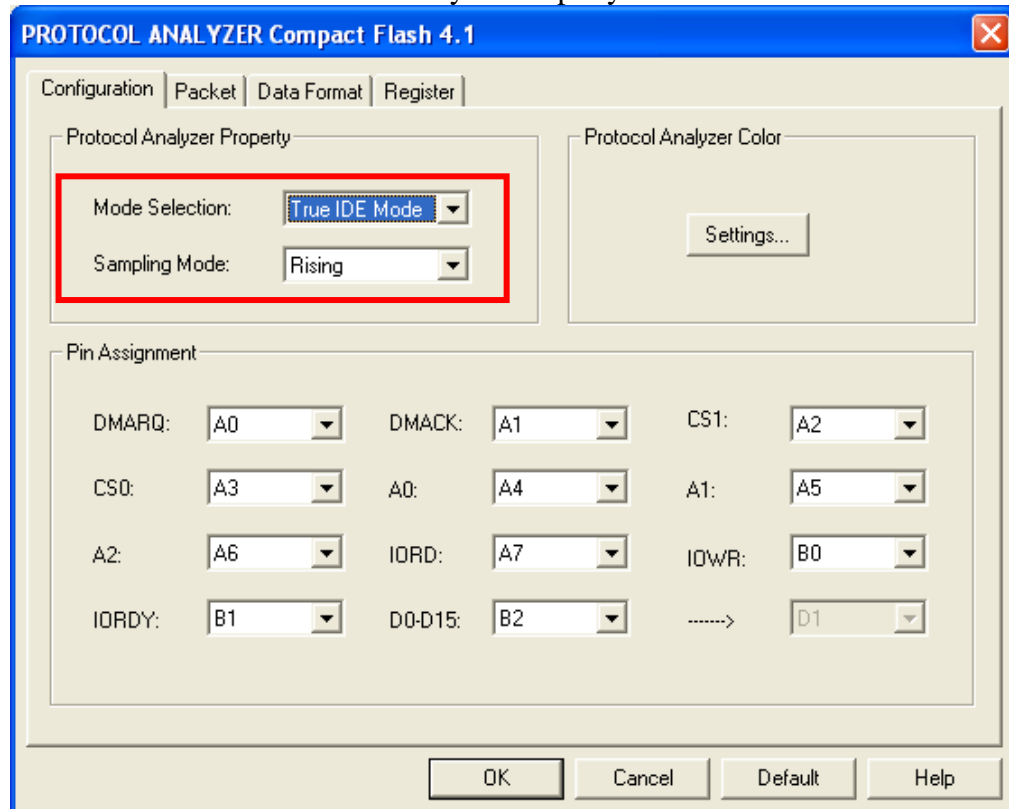




STEP 3. For Protocol Analyzer Parameters Configuration, select Protocol Analyzer, and then choose **ZEROPLUS LA Compact Flash 4.1 MODULE V1.01.00 (CN01)**. Next click **Parameters Configuration** to open **Configuration** dialog box.

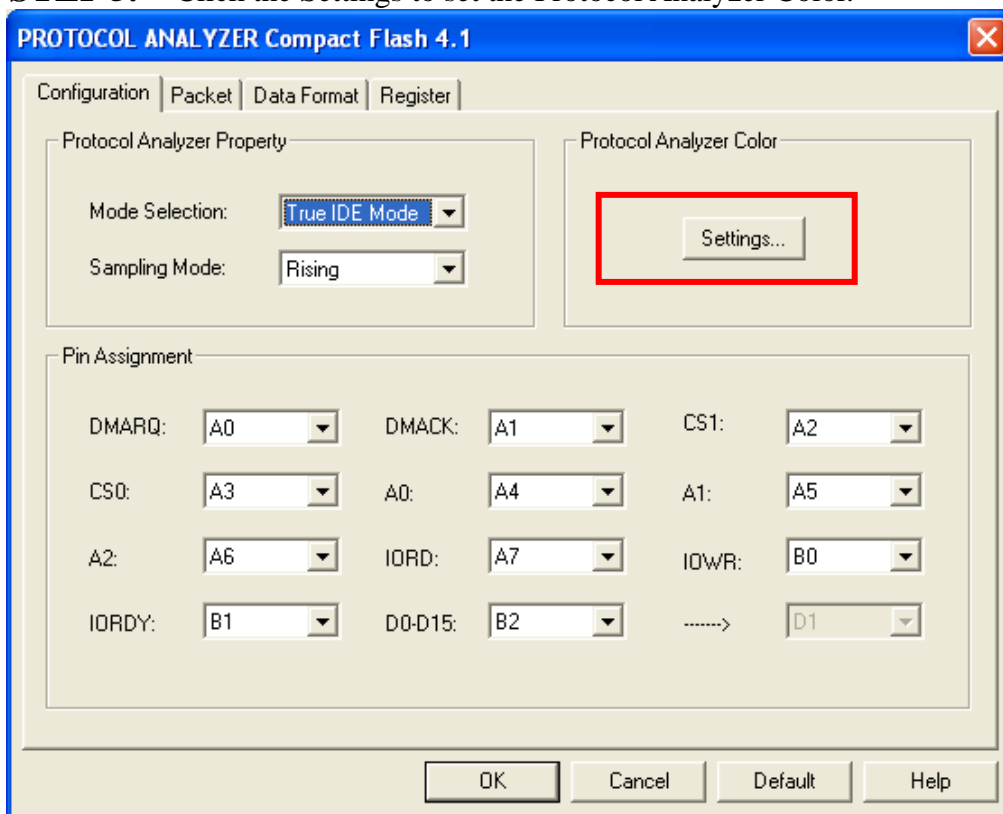


STEP 4. Set the Protocol Analyzer Property.

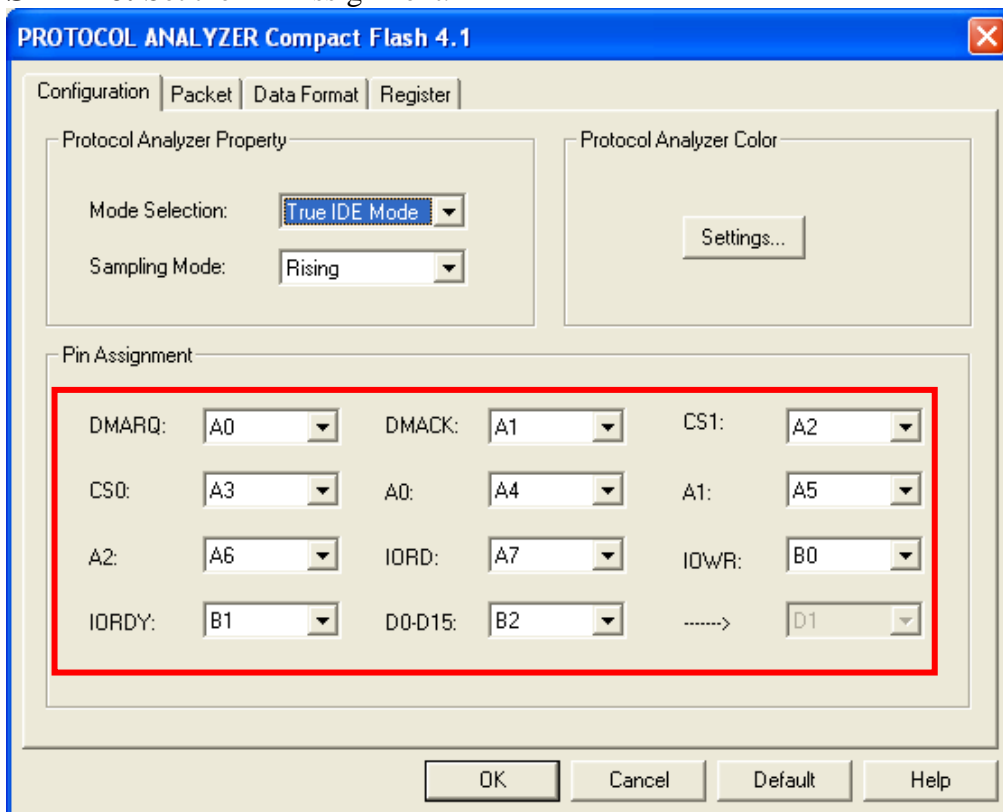




STEP 5. Click the Settings to set the Protocol Analyzer Color.



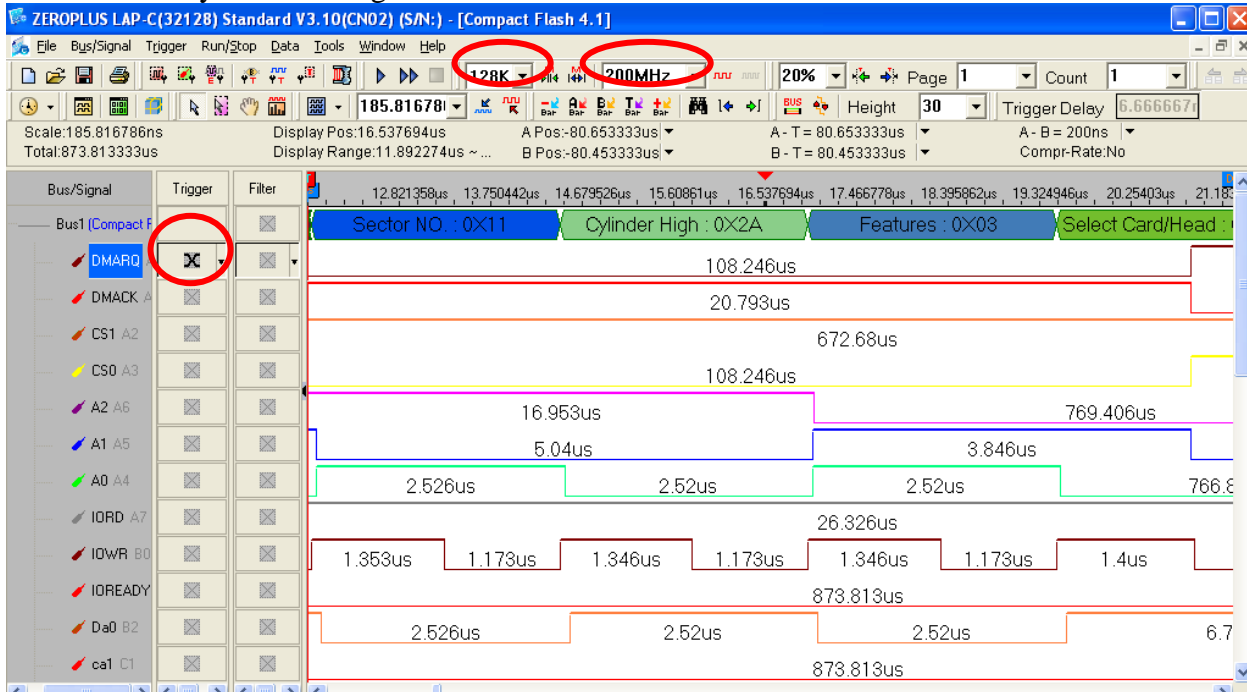
STEP 6. Set the Pin Assignment.





STEP 7. Following pictures show the completion of the protocol analyzer decoding and the packet list. The trigger condition is set as Either Edge; the memory depth is 128K; the sampling frequency is 200MHz. (the sampling frequency should be more than eight times higher than the signal to be tested)

Protocol Analyzer Decoding



Packet List

